

# Bridge Transducer Switch

## FEATURES

- Dual Matched Current Sources
- High-gain Differential Sensing Circuit
- Wide Common-mode Input Capability
- Complementary Digital Open-collector Outputs
- Externally Programmable Time Delay
- Optional Output Latch with Reset
- Built-in Diagnostic Activation
- Wide Supply Voltage Range
- High Current Heater Power Source Driver

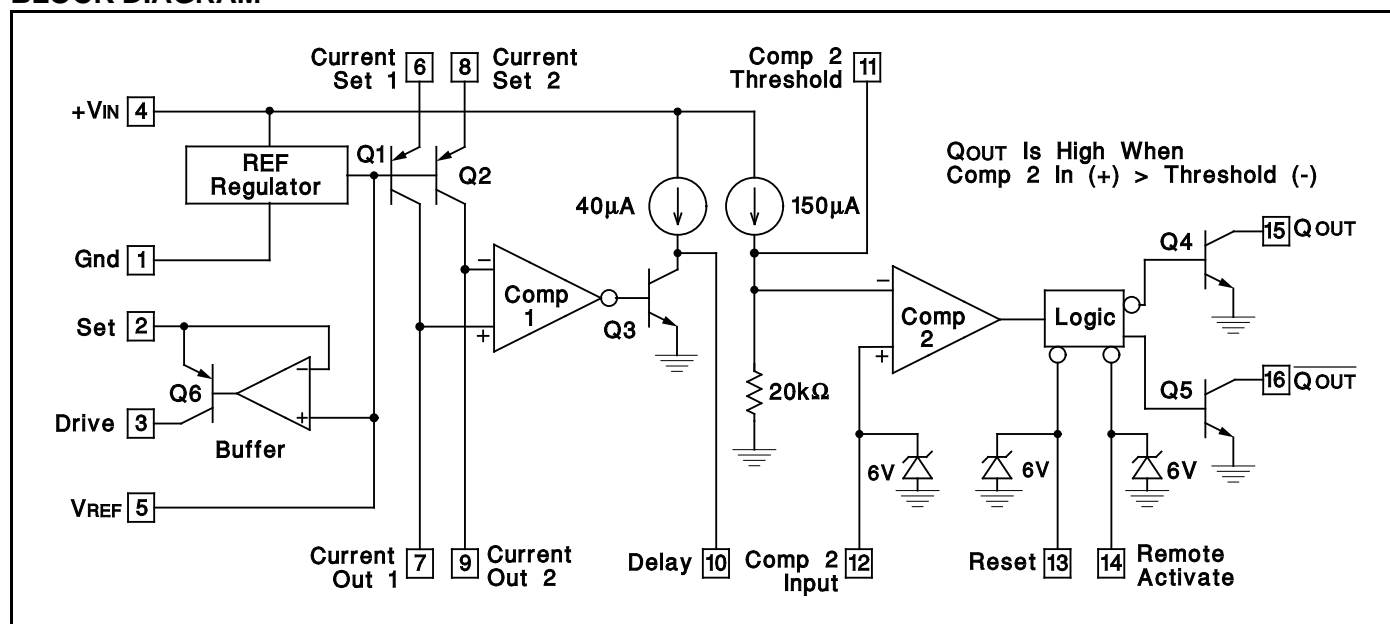
## DESCRIPTION

This integrated circuit contains a complete signal conditioning system to interface low-level variable impedance transducers to a digital system. A pair of matched, temperature-compensated current sources are provided for balanced transducer excitation followed by a precision, high-gain comparator. The output of this comparator can be delayed by a user-selectable duration, after which a second comparator will switch complementary outputs separately activated for diagnostic operation and has an optional latch with external reset capability. An added feature is a high current power source useful as a heater driver in differential temperature sensing applications. The UC3704 is designed for 0°C to +70°C environments.

## UC3704 COMPATIBLE SENSORS

SENSOR TYPE	ACTIVATION SOURCE						
	Temperature	Pressure	Force	Position	Displacement	Velocity	Shock
Thermistor	X					X	
Sensistor	X					X	
Thermocouple	X						
Semiconductor	X	X	X				
Photo Voltaic				X	X	X	
Photo Resistive				X	X	X	
Strain Gage		X	X	X	X	X	X
Piezoelectric		X	X		X	X	X
Magneto Resistive				X	X		
Inductive				X	X	X	X
Hall Effect				X	X		
Capacitive							X

## BLOCK DIAGRAM



**ABSOLUTE MAXIMUM RATINGS**

Supply Voltage (+V<sub>IN</sub>) . . . . . 40V  
 Output Current (each output) . . . . . 50 mA  
 Buffer Power Source Current . . . . . 200mA  
 Comparator 1 Inputs . . . . . -0.5V to V<sub>REF</sub>  
 Comparator 2 Inputs . . . . . 0 to 5.5V  
 Remote Activation and Reset Inputs . . . . . 0 to 5.5V  
 Power Dissipation at T<sub>A</sub> = 25°C . . . . . 1000mW  
 Operating Junction Temperature . . . . . -55°C to +150°C  
 Storage Temperature Range . . . . . -65°C to +150°C  
 Lead Temperature (Soldering, 10 Seconds) . . . . . +300°C

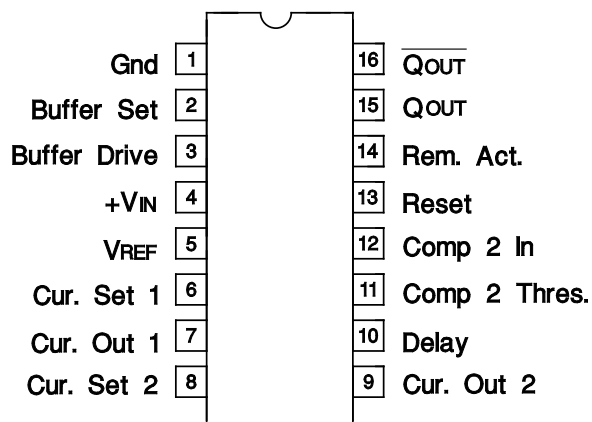
*Note: Unless otherwise specified, all voltages are with respect to ground (Pin 1).*

*Currents are positive into, negative out of the specified terminal.*

*Consult Packaging section of Databook for thermal imitations and considerations of package.*

**CONNECTION DIAGRAM**

**DIL-16 (TOP VIEW)**  
**J or N Package**



**ELECTRICAL CHARACTERISTICS:** Unless otherwise stated, these specifications apply for T<sub>A</sub> = 0°C to +70°C for the UC3704: V<sub>IN</sub> = 15V, T<sub>A</sub> = T<sub>J</sub>.

PARAMETERS	TEST CONDITIONS	MIN	TYP	MAX	UNITS
<b>Power Inputs</b>					
Supply Voltage Range		4.2		36	V
Supply Current	V <sub>IN</sub> = 36V		5	10	mA
<b>Reference Section (with respect to V<sub>IN</sub>)</b>					
V <sub>REF</sub> Value  V <sub>IN</sub> - V <sub>REF</sub>	T <sub>J</sub> = 25°C	2.1	2.2	2.3	V
V <sub>REF</sub> Temperature Coefficient	Note 1	-1	-2	-3	mV/°C
Line Regulation	ΔV <sub>IN</sub> = 4.2 to 25V		2	10	mV
Load Regulation	ΔI <sub>O</sub> = 0 to 4mA		2	10	mV
Short Circuit Current	V <sub>IN</sub> = 36V, V <sub>REF</sub> = V <sub>IN</sub> or Ground			±25	mA
<b>Current Source (Q1 and Q2)</b>					
Output Current (Note 2)	Current Set = 10μA	-9	-9.5	-10	μA
	Current Set = 200μA	-180	-195	-200	μA
Output Offset Current	R <sub>E6</sub> = R <sub>E8</sub> = 20kΩ		0	±1	μA
<b>Comparator One</b>					
Input Offset Voltage			±1	±4	mV
Input Bias Current			-100	-300	nA
Input Offset Current				±60	nA
CMRR	V <sub>CM</sub> = 0 to 12V	60	70		dB
Voltage gain	R <sub>L</sub> > 150kΩ	70	85		dB
Delay Current Source		34	40	52	μA
Output Rise Time	Overdrive = 10mV, C <sub>D</sub> = 15pF, T <sub>J</sub> = 20°C		2		V/μs

**ELECTRICAL CHARACTERISTICS:** Unless otherwise stated, these specifications apply for  $T_A = 0^\circ\text{C}$  to  $+70^\circ\text{C}$  for the UC3704:  $V_{IH} = 15\text{V}$ ,  $T_A = T_J$ .

PARAMETER	TEST CONDITIONS		MIN	TYP	MAX	UNITS
Comparator Two (Q <sub>OUT</sub> and $\overline{Q_{OUT}}$ )						
Threshold Voltage			2.2	3.0	3.8	V
Threshold Resistance	To Ground		14	20	24	kΩ
Input Bias Current	V <sub>IN</sub> (Pin 12) = 5V			1	3	μA
Remote Activate Current	Pin 14 = 0V			0.2	0.5	mA
Reset Current	Pin 13 = 0V			0.2	0.5	mA
Remote Activate Threshold	T <sub>A</sub> = 25°C		0.8	1.2		V
Reset Threshold	T <sub>A</sub> = 25°C		0.8	1.2		V
Output Saturation	I <sub>OUT</sub> = 16mA			0.2	0.5	V
	I <sub>OUT</sub> = 50mA			0.7	2.0	V
Output Leakage	V <sub>OUT</sub> = 40V			0.2	10	μA
Output Response	Comp. Overdrive = 1V R <sub>L</sub> = 5k to V <sub>IN</sub>	Turn-on		0.4		μs
		Turn-off		1.0		μs
Buffer						
Set Voltage (V <sub>IN</sub> -V <sub>S</sub> )	T <sub>J</sub> = 25°C, I <sub>S</sub> = 100mA		1.9	2.1	2.3	V
Drive Current	T <sub>J</sub> = 25°C, R <sub>S</sub> = 200Ω, V <sub>D</sub> = 0V		90	100	120	mA

Note 1: Parameter guaranteed by design, not tested in production.

Note 2: Collector output current =  $\frac{V_{IN} - V_{REF} - V_{BE}}{R_E} \approx \frac{1.5\text{V}}{R_E}$

## APPLICATIONS INFORMATION

### Sensor Section

The input portion of the UC3704 provides both excitation and sensing for a low-level, variable impedance transducer. This circuitry consists of a pair of highly matched PNP transistors biased for operation as constant current sources followed by a high gain precision comparator.

The reference voltage at the bases of the PNP transistors has a TC to offset the base-emitter voltage variation of these transistors resulting in a constant voltage across the external emitter resistors and correspondingly constant collector currents for balancing, offsetting, or to provide unique temperature characteristic.

With the PNP transistor's optimum current ranging from 10 to 200 $\mu\text{A}$ , and the common-mode input voltage of the comparator usable from ground to ( $V_{IN} - 3\text{V}$ ), a wide range of transducer impedance levels is possible.

The sensor comparator has a current source pull-up at the output so that an external capacitor from this point to

ground can be used to provide a programmable delay before reaching the second comparator's threshold. The low-impedance on-state of Comp 1's output provides quick reset of this capacitor. This programmable delay function is useful for providing transient protection by requiring that Comp 1 remain activated for a finite period of time before Comp 2 triggers. Another application is in counting repetitive pulses where a missing pulse will allow Comp 1's output to rise to Comp 2's threshold. This time delay function is:

$$\text{Delay} = \frac{\text{Comp2 Threshold}}{\text{Delay Current}} \times C_D \approx 175\text{ms}/\mu\text{F}$$

If hysteresis is desired for Comparator 1, it may be accommodated by applying positive feedback from the delay terminal to the non-inverting input on Pin 7. This will aid in providing oscillation-free transitions for very slowly changing inputs.

## APPLICATIONS INFORMATION (cont.)

### Output Section

The output portion of the UC3704 is basically a second comparator with complimentary, open-collector outputs. This comparator has a built-in, ground-referenced threshold implemented with a high-impedance current source and resistor so that it may be easily overridden with an external voltage source if desired. Comp 2's input transistors are NPN types which require at least 1V of common-mode voltage for accurate operation and should not see a differential input voltage greater than 6V.

For diagnostic or latching purposes, the output logic is equipped with a Remote Activate and Reset function. These pins have internal pull-ups and are only active when pulled low below a threshold of approximately 1V. A low signal at the Remote Activate Pin causes the outputs to change state in exactly the same manner as if Comp 2's input is raised above the threshold on Pin 11. If Pin 16 is connected to Pin 14, positive feedback results and the outputs will latch once triggered by Comp 2's input.

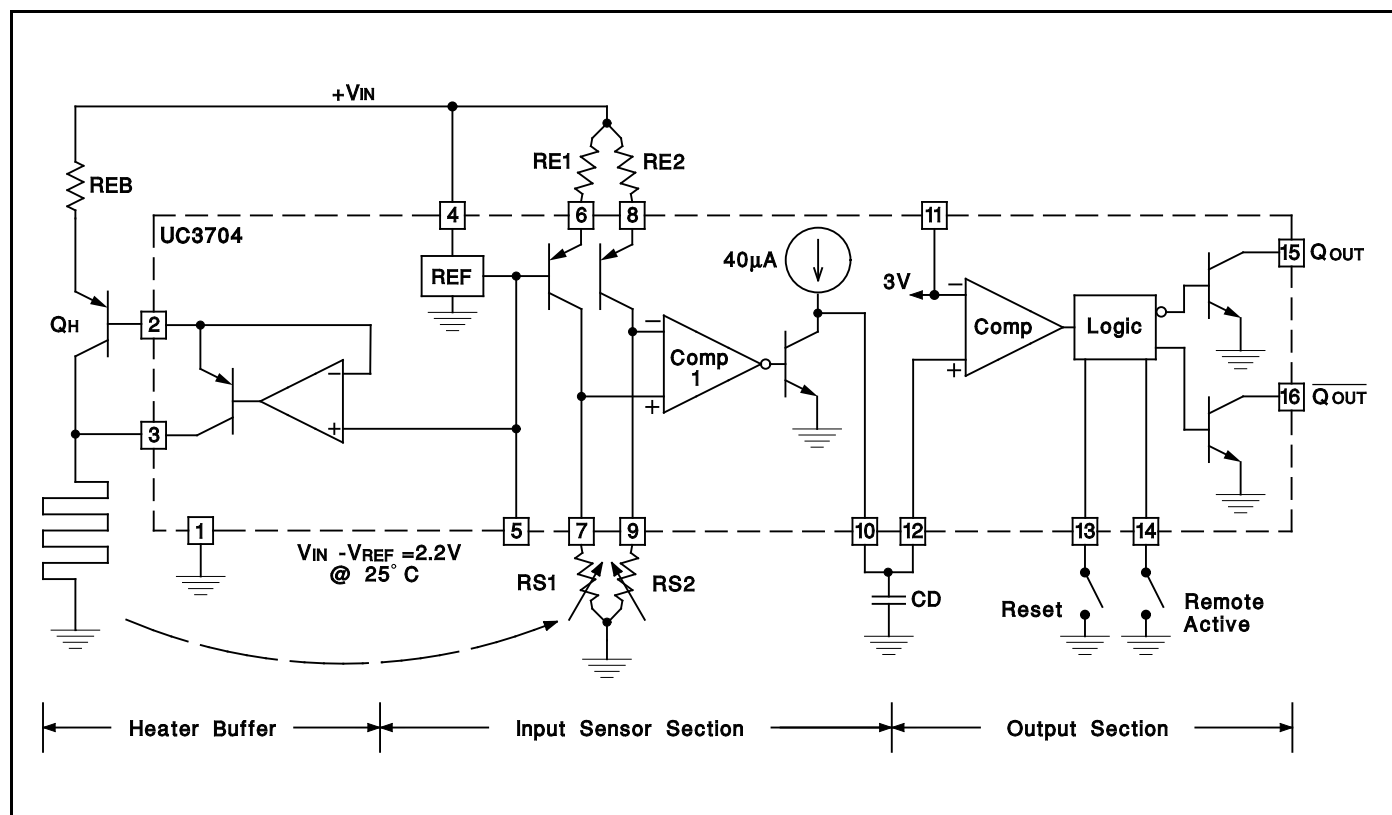
Pulling the Reset terminal low overrides the Remote Activate Pin releasing the latch.

### Reference Buffer

This circuit is designed to provide up to 100mA to drive a high current external PNP transistor useful for powering a heater for differential temperature measurements. Care must be taken that power dissipation in Q6 does not cause excessive thermal gradients which will degrade the accuracy of the sensing circuitry.

Using a heating element attached to a temperature sensitive resistor, RS1, in one leg of the input bridge implements a flow sensor for either gasses or liquids. As long as there is flow, heat from the element is carried away and the sensor voltage remains below threshold. Using an identical sensor, RS2, without a heater to establish this threshold compensates for the ambient temperature of the flow.

## TYPICAL APPLICATION FOR MONITORING LIQUID OR GAS FLOW



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